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Dugdale, M.;
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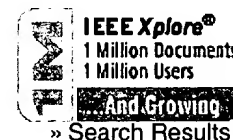
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Jer Min Jou; Yeu-Horng Shiau; Pei-Yin Chen; Shiann-Rong Kuang;
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Pages:1703 - 1714[\[Abstract\]](#) [\[PDF Full-Text \(812KB\)\]](#) **IEEE JNL****33 Representations and routing for Cayley graphs [computer networks]***Arden, B.W.; Tang, K.W.;*
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Pages:1533 - 1537[\[Abstract\]](#) [\[PDF Full-Text \(404KB\)\]](#) **IEEE JNL****34 Serial architectures for the implementation of 2-D digital filters and for template matching in digital images***Sid-Ahmed, M.A.;*
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Pages:853 - 857[\[Abstract\]](#) [\[PDF Full-Text \(320KB\)\]](#) **IEEE JNL****35 Fault modeling of physical failures in CMOS VLSI circuits***Zaghloul, M.E.; Gobovic, D.;*
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